

	Subclass
Class	
ISSUE CLASSIFICATION	

U.S. UTILITY PATENT APPLICATION

O.I.P.E.	PATENT DATE
<i>KS</i> SCANNED <i>LH2</i> Q.A. <i>AD 21</i>	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/972963	F	438 25	1357	2813826	<i>Wilson K. S. Chae</i>

APPLICANTS

Gee-Sung Chae

TITLE

Thin film transistor array substrate for liquid crystal display device and method of manufacturing the same

PTO-2040
12/59

PREPARED AND APPROVED FOR ISSUE

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket					

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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<input type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____	(Primary Examiner) _____ (Date) _____			ISSUE FEE	
<input type="checkbox"/> c) The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date) _____			ISSUE BATCH NUMBER	
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